

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	15	(contiguous adj program)	USPAT	OR	OFF	2005/06/21 12:13
L4	237	(input adj clock) same (output adj clock) same (alignment or position or placement or configuration)	USPAT	OR	OFF	2005/06/21 12:14
L5	2	4 and ((output adj latch) same (externally or external) same clock)	USPAT	OR	OFF	2005/06/21 12:53
L6	14	(transfer or transferring or convey or conveying) same (data or information) same output same latch same (output adj clock) same (read)	USPAT	OR	OFF	2005/06/21 12:56
S8	0	(memory same read same (input adj clock) same (external or externally)) and ((output adj latch) same (output adj clock)) and (clock same domain same alignment same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:14
S9	0	(memory same read same (input adj clock) same (external or externally)) and ((output adj latch) same (output adj clock)) and (clock same alignment same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:14
S10	0	(memory same read same (input adj clock) same (external or externally)) and ((output adj latch) same (output adj clock)) and (clock same domain same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:15
S11	0	(memory same read same (input adj clock) same (external or externally)) and ((output adj latch) same (output adj clock)) and (clock same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:15
S12	1	(memory same read same (input adj clock)) and ((output adj latch) same (output adj clock)) and (clock same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:22

S13	0	(memory same read same (input adj clock)) and ((output adj latch) same (output adj clock)) and (alignment same (output adj latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:22
S14	0	(memory same read same (input same clock) same (external or externally)) and ((output adj latch) same (output same clock)) and (alignment same (output adj latch) same (input adj clock) same (output same clock))	USPAT	OR	OFF	2005/06/20 15:23
S15	0	(memory same read same (input same clock) same (external or externally)) and ((output same latch) same (output same clock)) and (alignment same (output same latch) same (input same clock) same (output same clock))	USPAT	OR	OFF	2005/06/20 15:24
S16	0	(read same (input same clock) same (external or externally)) and ((output same latch) same (output same clock)) and (alignment same (output same latch) same (input same clock) same (output same clock))	USPAT	OR	OFF	2005/06/20 15:24
S17	0	(read same (input same clock) same (external or externally)) and ((latch) same (output same clock)) and (alignment same (latch) same (input same clock) same (output same clock))	USPAT	OR	OFF	2005/06/20 15:28
S18	230	(read same (input same clock) same (external or externally)) and ((latch) same (output same clock)) and ((alignment or position or placement or configuration) same (latch) same (input same clock) same (output same clock))	USPAT	OR	OFF	2005/06/20 15:34
S19	1	(read same (input adj clock) same (external or externally)) and ((latch) same (output adj clock)) and ((alignment or position or placement or configuration) same (latch) same (input adj clock) same (output adj clock))	USPAT	OR	OFF	2005/06/20 15:29
S20	228	S18 and memory	USPAT	OR	OFF	2005/06/21 11:26